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(54) **SIGNAL SYNCHRONIZING SYSTEM**

(57) Abstract:

PURPOSE: To effectively synchronize the instantaneous values of the voltage and the current of plural stations by allowing a synchronous slave station to receive a synchronous data frame from a master station, to adjust the synchronizing signal of it's own station based on the reception time of the synchronous data frame and to take the synchronization with a main synchronizing signal.

CONSTITUTION: When a talken bus control circuit 12 finishes receiving a data frame D at one synchronous slave station, it generates an interruption request IRQ. When a processor 13 for a talken passing bus receives the request IRQ, it decides the frame D and generates a reception synchronizing pulse QR when it detects the synchronous frame data D₁. This pulse QR is phase-compared 22 with a station synchronizing signal S outputted by a station synchronizing signal generator 20. A signal DPH according to the phase difference between them and the phase of the signal S is adjusted within a fixed limit based on the signal DPH. Thus, the station synchronizing signal of the synchronous slave station is made to coincide with earlier time being equal to the sum of the decision processing lime and the

station delay time of the device 13 for the pulse QR and it coincides and synchronizes with the main synchronizing signal S₁.

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